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XILINX, INC
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Pat Slaback	(Depositor's name)
<i>Pat Slaback</i>	(Signature)
June 14, 2004	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/092,051	03/04/2002	Stephen M. Douglass	X-923 US	7802

TITLE OF INVENTION: METHOD OF DESIGNING INTEGRATED CIRCUIT HAVING BOTH CONFIGURABLE AND FIXED LOGIC CIRCUITRY

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$0	\$1330	06/18/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
WHITMORE, STACY	2812	716-018000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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1. Garlick & Harrison
2. Kim Kanzaki
3. _____

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(B) RESIDENCE: (CITY and STATE OR COUNTRY)

XILINX, INC.

San Jose, California 95124

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ individual ☒ corporation or other private group entity ☐ government

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COMMISSIONER FOR PATENTS
P. O. Box 1450
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Inventor(s): Stephen M. Douglass

Assignee: XILINX, INC.

Serial No.: 10/092,051

conf.no. 7802

Filed: March 4, 2002

Title: Method of Designing Integrated Circuit Having Both Configurable and Fixed Logic Circuitry

Docket No.: X-923 US

Enclosed: Return Receipt Postcard

Fee Transmittal - Part B

Date: June 14, 2004

VIA FACSIMILE 703-746-4000

Atty/Sec: KK/pbs